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(54) Title: SIGNALING PROCESSOR FOR DIGITAL MOBILE RADIO SYSTEMS

(57) Abstract

There is described a processor for signaling generated by transmission measurements included in a base radio station controller (BSC) of the pan-European mobile radio system (GSM®). The processor comprises a first microprocessor connected, through a dual access read and write memory, to a second microprocessor (DSP) and also interfaced with numerical signaling lines and a processor which manages the radio resources. The first microprocessor manages levels 1 and 2 of the signaling protocol LAPD and transfers to the DSP the level 3 messages which concern the transmission measurements of power and quality of the radio signal and of distance between the connected points. The DSP writes said messages in a dynamically managed RAM and at the same time processes the messages received to take back-messages comprising Handover decisions and indications for Power Control of mobile equipment.

